CLAIM LISTING

1. (Original) A m thod of making a thin film layered superlattice material on an integrated circuit substrate using trimethylbismuth, said method comprising the steps of:

dissolving said trimethylbismuth and a metal compound in a solvent to produce a final precursor liquid;

forming a mist of said final precursor liquid with a carrier gas to produce a precursor mist;

vaporizing said precursor mist to produce a precursor vapor;

providing said integrated circuit substrate;

applying said precursor vapor onto said substrate to produce said thin film superlattice material without any heating step that includes a temperature of 650°C or higher; and

completing the fabrication of said integrated circuit to include at least a portion of said thin film layered superlattice material in a component of said integrated circuit.

- 2. (Original) A method as in claim 1 wherein said step of dissolving said trimethylbismuth comprises mixing said trimethylbismuth with an organic solvent.
- 3. (Original) A method as in claim 1 wherein said metal compound comprises a metal polyalkoxide compound containing at least two metals selected from the group consisting of strontium, calcium, barium, cadmium, lead, tantalum, hafnium, tungsten, niobium, zirconium, bismuth, scandium, yttrium, lanthanum, antimony, chromium, molybdenum, vanadium, ruthenium and thallium.
- 4. (Original) A method as in claim 1 wherein said metal compound comprises a diketonate compound containing at least two metals selected from the group consisting of strontium, calcium, barium, cadmium, lead, tantalum, hafnium, tungsten, niobium, zirconium, scandium, yttrium, lanthanum, antimony, chromium, molybdenum, vanadium, ruthenium and thallium.
- 5. (Original) A method as in claim 1 wherein said metal compound comprises an oxide compound containing at least two metals selected from the group consisting of strontium, calcium, barium, cadmium, lead, tantalum, hafnium, tungsten,

Fige 3 of 8 Signal No. 10/007,119 195080v2 Amendment and Remarks Responsive to Office Action mailed 02-10-2004 niobium, zirconium, scandium, yttrium, lanthanum, antimony, chromium, molybdenum, vanadium, ruthenium and thallium.

- 6. (Original) A method as in claim 1 in which the amount of trimethylbismuth is less than 15 mol%.
- 7. (Original) A method as in claim 1 in which the amount of trimethylbismuth is less than 10 mol%.
- 8. (Original) A method as in claim 1 in which the amount of trimethylbismuth is 8 mol%.
- 9. (Original) A method as in claim 2 wherein said organic solvent comprises at least one compound selected from the group consisting of tetrahydrofuran, methyl ethyl ketone, isopropanol, methanol, xylene, n-butyl acetate, octane, 2-methoxyethanol, toluene, diethylethane, 1,4-dioxane and hexane.
- 10. (Original) A method as in claim 1 wherein said step of forming a mist of said precursor liquid comprises flowing said carrier gas across an open throat of a tube containing said precursor liquid, with the flow of said gas being substantially parallel to said open throat.
- 11. (Original) A method as in claim 1 and further including a step of flowing oxygen gas into said deposition chamber reactor prior to said step of reacting.
- 12. (Original) A method as in claim 1 and further comprising at least one step of treating said thin film at temperatures below 650°C to crystallize or partially crystallize said superlattice material in a phase including more grains with a high polarizability orientation than prior to said at least one step of treating, whereby said at least one step of treating is RTP, oxygen furnace annealing prior to formation of an electrode or contact to said layered superlattice material thin film, or an anneal after an electrode or other contact to the superlattice material thin film is formed.
- 13. (Original) A method as in claim 1 wherein said step of mixing said precursor mist comprises mixing said precursor mist with an inert carrier gas.
- 14. (Original) A method as in claim 1 wherein said carrier gas comprises at least one compound selected from the group consisting of nitrogen and argon.

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- 15. (Original) A method as in claim 1 wherein said step of vaporizing comprises heating said precursor aerosol prior to said deposition step.
- 16. (Original) A method as in claim 15 wherein said step of heating further comprises heating said precursor aerosol to a temperature of from 50°C to 250°C.
- 17. (Original) A method as in claim 15 wherein said step of heating further comprises heating said precursor aerosol to a temperature of from 100°C to 200°C.
- 18. (Original) A method as in claim 1 wherein said step of providing said substrate further comprises placing said substrate inside a deposition chamber.
- 19. (Original) A method as in claim 18 wherein said step of applying said precursor vapor comprises chemical vapor deposition.
- 20. (Original) A method as in claim 1 wherein said step of applying said precursor vapor further comprises deposition on a heated substrate.
- 21. (Original) A method as in claim 1 wherein said step of applying said precursor vapor further comprises deposition on an ambient temperature substrate.
- 22. (Original) A method as in claim 1 wherein the thin film layered superlattice material is 1 nm to 100 nm thick.
- 23. (Original) A method of making a thin film layered superlattice material on an integrated circuit substrate using trimethylbismuth, said method comprising the steps of:

dissolving said trimethylbismuth and a metal compound in a solvent to produce a precursor liquid, with the relative concentration of said trimethylbismuth to said solvent being 15 mol% or less;

forming a mist of said precursor liquid with a carrier gas to produce a precursor mist;

vaporizing said precursor mist to produce a precursor vapor; providing said integrated circuit substrate;

applying said precursor vapor onto said substrate to produce said thin film superlattice material without any heating step that includes a temperature of 650°C or higher; and

Frage 5 of 8 Serial No. 10/007,119 195080v2 Amendment and Remarks Responsive to Office Action mailed 02-10-2004 completing the fabrication of said int grat d circuit to include at least a portion of said thin film layered superlattice material in a component of said integrated circuit.

Claims 24 – 30 (Canceled)

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